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1	rip\$7 and rout\$4 and longer same (wir\$4 rout\$4 net path line trace)
2	rip\$7 and rout\$4 and longer same (wir\$4 rout\$4 net path line trace) and (timing delay) near4 (violat\$5 meet\$4 met satisf\$8 constran\$4 budget)
3	rip\$7 and rout\$4 and longer same (wir\$4 rout\$4 net path line trace) and (timing delay) near4 (violat\$5 meet\$4 met satisf\$8 constran\$4 budget) same path
4	rip\$7 and rout\$4 and longer same (wir\$4 rout\$4 net path line trace) and (timing delay) same (violat\$5 meet\$4 met satisf\$8 constran\$4 budget) same path
5	rip\$7 and rout\$4 and longer same (wir\$4 rout\$4 net path line trace) and (tim\$5 delay) same (violat\$5 meet\$4 met satisf\$8 constran\$4 budget) same path
6	rip\$7 and rout\$4 and longer with (wir\$4 rout\$4 net path line trace) and (tim\$5 delay) same (violat\$5 meet\$4 met satisf\$8 constran\$4 budget) same path
7	rip\$7 and rout\$4 and longer with (wir\$4 rout\$4 net path line trace) and (tim\$5 delay) same (violat\$5 meet\$4 met satisf\$8 constran\$4 budget) same path and (buffer driver inverter repeater)
8	rip\$7 and rout\$4 and longer with (wir\$4 rout\$4 net path line trace) and (tim\$5 delay) same (violat\$5 meet\$4 met satisf\$8 constran\$4 budget) same path and (buffer driver inverter repeater) and (slew crosstalk\$4 cross adj talk\$4)
9	(rip\$7 remov\$4 delet\$4) with (wir\$4 rout\$4 net\$4 path) and (longer) same length same (wir\$4 rout\$4 net\$4 path) and (tim\$5 delay) same (violat\$5 meet\$4 met satisf\$8 constrain\$4 budget) same path
10	(rip\$7 remov\$4 delet\$4) with (wir\$4 rout\$4 net\$4 path) and (longer) same length same (wir\$4 rout\$4 net\$4 path) and (tim\$5 delay) same (violat\$5 meet\$4 met satisf\$8 constrain\$4 budget) same path and "716"/\$.ccls.
11	(rip\$7 remov\$4 delet\$4) with (wir\$4 rout\$4 net\$4 path) and (longer) same length same (wir\$4 rout\$4 net\$4 path) and (tim\$5 delay) same (violat\$5 meet\$4 met satisf\$8 constrain\$4 budget) same path and @ad<"20030827"
12	(rip\$7 remov\$4 delet\$4) with (wir\$4 rout\$4 net\$4 path) and (longer) same length same (wir\$4 rout\$4 net\$4 path) and (tim\$5 delay) same (violat\$5 meet\$4 met satisf\$8 constrain\$4 budget) same path and @ad<"20030827" and "716"/\$.ccls.
13	(rip\$7 remov\$4 delet\$4) with (wir\$4 rout\$4 net\$4 path) and (longer) same length same (wir\$4 rout\$4 net\$4 path) and (tim\$5 delay) same (violat\$5 meet\$4 met satisf\$8 constrain\$4 budget) same path and @ad<"20030827" and "716"/\$.ccls. and (coupl\$5 adj2 noise or crosstalk\$4 or cross adj talk\$4)
14	(coupl\$5 near3 ( noise capacit\$4 induct\$5) or crosstalk\$4 or cross adj talk\$4) and @ad<"20030827"
15	(coupl\$5 near3 ( noise capacit\$4 induct\$5) or crosstalk\$4 or cross adj talk\$4) and @ad<"20030827" and longer with (net wir\$4 rout\$4 path length)
16	(coupl\$5 near3 ( noise capacit\$4 induct\$5) or crosstalk\$4 or cross adj talk\$4) and @ad<"20030827" and longer with (net wir\$4 rout\$4 path length) and "716"/\$.ccls.
17	(coupl\$5 near3 ( noise capacit\$4 induct\$5) or crosstalk\$4 or cross adj talk\$4) and @ad<"20030827" and (new\$4 replac\$7) with longer with (net wir\$4 rout\$4 path length) and "716"/\$.ccls.
18	(coupl\$5 near3 ( noise capacit\$4 induct\$5) or crosstalk\$4 or cross adj talk\$4) and @ad<"20030827" and longer near4 (net wir\$4 rout\$4 path length distan\$5) and "716"/\$.ccls.

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19	(coupl\$5 near3 ( noise capacit\$4 induct\$5) or crosstalk\$4 or cross adj talk\$4) and @ad<"20030827" and (new\$4 replac\$7) same longer same (net wir\$4 rout\$4 path length) and "716"/\$.ccls.
20	(coupl\$5 near3 ( noise capacit\$4 induct\$5) or crosstalk\$4 or cross adj talk\$4) and @ad<"20030827" rip\$8 same rerout\$4 same longer
21	(coupl\$5 near3 ( noise capacit\$4 induct\$5) or crosstalk\$4 or cross adj talk\$4) and @ad<"20030827" and rip\$8 same rerout\$4 same longer
22	(coupl\$5 near3 ( noise capacit\$4 induct\$5) or crosstalk\$4 or cross adj talk\$4) and @ad<"20030827" and rip\$8 same re-rout\$4 same longer
23	@ad<"20030827" and rip\$8 same re-rout\$4 same longer @ad<"20030827"
24	rip\$8 same (re-rout\$4 rerout\$4) same longer and @ad<"20030827"
25	remov\$4 same replac\$7 same longer and @ad<"20030827" "."
26	remov\$4 same replac\$7 same longer and @ad<"20030827"
27	remov\$4 same replac\$7 same longer and @ad<"20030827" and "716"/\$.ccls.
28	"20050050496" and higher same layer
29	(coupl\$5 near3 ( noise capacit\$4 induct\$5) or crosstalk\$4 or cross adj talk\$4) and @ad<"20030827" and (high\$4 upper\$8 surface top\$8) same layer same longer
30	(coupl\$5 near3 ( noise capacit\$4 induct\$5) or crosstalk\$4 or cross adj talk\$4) and @ad<"20030827" and ( (high\$4 upper\$8 surface top\$8) near3 layer ) same longer
31	(coupl\$5 near3 ( noise capacit\$4 induct\$5) or crosstalk\$4 or cross adj talk\$4) and @ad<"20030827" and ( (high\$4 upper\$8 surface top\$8) near3 layer ) same longer and "716"/\$.ccls.
32	(coupl\$5 near3 ( noise capacit\$4 induct\$5) or crosstalk\$4 or cross adj talk\$4) same ((high\$4 upper\$8 surface top\$8) near3 layer ) and @ad<"20030827" and "716"/\$.ccls.
33	(coupl\$5 near3 ( noise capacit\$4 induct\$5 noise) or crosstalk\$4 or cross adj talk\$4) same ((high\$4 upper\$8 surface top\$8) near3 layer ) same (reduc\$4 mini\$5 decreas\$4) and @ad<"20030827" and "716"/\$.ccls.
34	(coupl\$5 near3 ( noise capacit\$4 induct\$5 noise) or noise or crosstalk\$4 or cross adj talk\$4) same ((high\$4 upper\$8 surface top\$8) near3 layer ) same (reduc\$4 mini\$5 decreas\$4) and @ad<"20030827" and "716"/\$.ccls.
35	(coupl\$5 near3 interconnect\$5) same ((high\$4 upper\$8 surface top\$8) near3 layer ) same (reduc\$4 mini\$5 decreas\$4) and @ad<"20030827" and "716"/\$.ccls.
36	("5784600".pn. "6256769".pn.) and cross\$7
37	"6058256".pn. and (check\$4 verify\$4 synthes\$5 simulat\$6)
38	"6058256".pn. and (driv\$5 buffer)
39	"20050050496" and driv\$4 same siz\$4
40	"6058256".pn. and (driv\$5 buffer)
41	(driv\$5 buffer) same siz\$4 same well adj known\$4 and @ad<"20030827"
42	(driv\$5 buffer) same siz\$4 same well adj known\$4 and @ad<"20030827" and "716"/\$.ccls.
43	(driv\$5 buffer) same siz\$4 same (affect\$4 affect\$5) same tim\$5 and @ad<"20030827" and "716"/\$.ccls.
44	(driv\$5 buffer) same (siz\$4 load\$4) same cross\$4 and @ad<"20030827" and "716"/\$.ccls.
45	(driv\$5 buffer) same (siz\$4 load\$4) same crosstalk and @ad<"20030827" and "716"/\$.ccls.

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46	(driv\$5 buffer) same (siz\$4 load\$4) same (keep remain\$4) and @ad<"20030827" and "716"/\$.ccls.
47	(driv\$5 buffer) same (siz\$4 load\$4) same (keep remain\$4) same (chang\$4 unchang\$4 ) and @ad<"20030827" and "716"/\$.ccls.

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1	(crosstalk\$4 or cross adj talk\$4 or (capacit\$5 induct\$5) near2 (coupl\$4)) and longer with (wir\$4 rout\$4 net path line conductor)
2	(crosstalk\$4 or cross adj talk\$4 or (capacit\$5 induct\$5) near2 (coupl\$4)) and longer with (wir\$4 rout\$4 net path line conductor) and "716"/\$.ccls.
3	(crosstalk\$4 or cross adj talk\$4 or (capacit\$5 induct\$5) near2 (coupl\$4)) and longer with (wir\$4 rout\$4 net path line conductor) and "716"/\$.ccls. and @ad<"20030827"
4	(crosstalk\$4 or cross adj talk\$4 or (capacit\$5 induct\$5) near2 (coupl\$4)) and longer with (wir\$4 rout\$4 net path line conductor) and "716"/\$.ccls. and @ad<"20030827" and (top\$7 higher surface upper) same (layer) same (rout\$4 wir\$4)
5	(crosstalk\$4 or cross adj talk\$4 or (capacit\$5 induct\$5) near2 (coupl\$4)) and longer with (wir\$4 rout\$4 net path line conductor) and "716"/\$.ccls. and @ad<"20030827" and (top\$7 higher surface upper) same (layer) same (rout\$4 wir\$4) and victim and aggressor
6	(crosstalk\$4 or cross adj talk\$4 or (capacit\$5 induct\$5) near2 (coupl\$4)) same longer same (wir\$4 rout\$4 net path line conductor) and "716"/\$.ccls. and @ad<"20030827" and (top\$7 higher surface upper) same (layer) same (rout\$4 wir\$4) and victim and aggressor
7	(crosstalk\$4 or cross adj talk\$4 ) same longer same (wir\$4 rout\$4 net path line conductor) same (top\$7 higher surface upper) same (layer) same (rout\$4 wir\$4 path net )and "716"/\$.ccls. and @ad<"20030827" and (top\$7 higher surface upper) same (layer) same (rout\$4 wir\$4)
8	(crosstalk\$4 or cross adj talk\$4 or (capacit\$5 induct\$5) near2 (coupl\$4)) and longer near2 (wir\$4 rout\$4 net path line conductor) and "716"/\$.ccls. and @ad<"20030827" and (top\$7 higher surface upper) same (layer) same (rout\$4 wir\$4)
9	(crosstalk\$4 or cross adj talk\$4 or (capacit\$5 induct\$5) near2 (coupl\$4)) and longer near2 (wir\$4 rout\$4 net path line conductor) and "716"/\$.ccls. and @ad<"20030827" and (top\$7 higher surface upper) same (layer) same (rout\$4 wir\$4) and victim and aggressor
10	(re adj rout\$4 re adj wir\$4 re adj layout\$4 rerout\$4 rewir\$4 relayout\$4 ) same (crosstalk\$4 or cross adj talk\$4) same (avoid\$4 reduc\$5 minim\$6 prevent\$4 fix\$4 correct\$5)
11	(re adj rout\$4 re adj wir\$4 re adj layout\$4 rerout\$4 rewir\$4 relayout\$4 ) same (crosstalk\$4 or cross adj talk\$4) same (avoid\$4 reduc\$5 minim\$6 prevent\$4 fix\$4 correct\$5) and "716"/\$.ccls.
12	(re adj rout\$4 re adj wir\$4 re adj layout\$4 rerout\$4 rewir\$4 relayout\$4 ) same (crosstalk\$4 or cross adj talk\$4) same (avoid\$4 reduc\$5 minim\$6 prevent\$4 fix\$4 correct\$5) and "716"/\$.ccls. and @ad<"20030827"
13	"6058256".pn. and (top\$7 higher surface upper\$4)
14	(top\$7 higher surface upper\$4) same (wir\$4 rout\$4) same (crosstalk\$4 or cross adj talk\$4 ) same (avoid\$4 reduc\$5 minim\$5 decreas\$4)
15	(top\$7 higher surface upper\$4) near3 layer same (wir\$4 rout\$4) same (crosstalk\$4 or cross adj talk\$4 ) same (avoid\$4 reduc\$5 minim\$5 decreas\$4)
16	((top\$7 higher surface upper\$4) near3 layer) same (wir\$4 rout\$4) same (crosstalk\$4 or cross adj talk\$4 ) same (avoid\$4 reduc\$5 minim\$5 decreas\$4)
17	((top\$7 higher surface upper\$4) near3 layer) same (wir\$4 rout\$4) same ((crosstalk\$4 or cross adj talk\$4 ) with (avoid\$4 reduc\$5 minim\$5 decreas\$4))
18	((top\$7 higher surface upper\$4) near3 layer) same (wir\$4 rout\$4) same ((crosstalk\$4 or cross adj talk\$4 ) with (avoid\$4 reduc\$5 minim\$5 decreas\$4)) and @ad<"20030827"

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19	((top\$7 higher surface upper\$4) near3 layer) same (wir\$4 rout\$4) same ((crosstalk\$4 or cross adj talk\$4 ) with (avoid\$4 reduc\$5 minim\$5 decreas\$4)) and @ad<"20030827" and "716"/\$.ccls.